

**AMENDMENTS TO THE SPECIFICATION**

Please replace the paragraph beginning on page 9 line 19 with the following:

AX The is Claimed() method is utilized to determine whether the device associated with the particular I/O path is described by the respective property file. First, the isClaimed() method receives via arguments information permitting the method to query a device associated with a given I/O path if necessary. For example, the argument information may be supplied to a SCSI gateway daemon associated with host agent 111 of FIGURE 1 to perform device queries if necessary for identification purposes. Such information may include a device file or host file. Alternatively, the IP address may be utilized as a method argument to allow querying via SNMP protocols for an SNMP device.

Please replace the paragraph beginning at page 12 line 15 with the following:

AX When implemented via executable instructions, various elements of the present invention are in essence the code defining the operations of such various elements. Specifically, it shall be appreciated that the aforementioned classes are object oriented code that operate on processor based systems via their various methods. Software or code operating on a processor or processors may implement operations of host agent 111 of FIGURE 1. Certain code may be operable to query devices associated with various LUNs to gather device information pursuant to SCSI protocols. Likewise, code may be operable to implement SNMP protocols.

Please replace the paragraph beginning at page 12 line 28 with the following:

AX FIGURE 4 illustrates processor based system 400 adapted according to embodiments of the present invention. Various devices associated with the present invention may utilize the architecture of processor based system 400, including but not limited to servers 104 and management server 103. Central processing unit (CPU) 401 is coupled to system bus 402. CPU 401 may be any general purpose CPU, such as an Intel Pentium processor. However, the present invention is not restricted by the architecture of CPU 401 as long as CPU 401 supports the inventive operations as described herein. CPU 401 executes the various operations such as the aforementioned methods of the various classes. Processor based system 400 includes BUS 402. Processor based system 400 also includes random access

AB memory (RAM) 403, which may be SRAM, DRAM, or SDRAM. Processor based system 400 includes ROM [[405]] 404 which may be PROM, EPROM, or EEPROM. RAM 403 and ROM 404 hold user and system data and programs as is well known in the art.

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Please replace the paragraph beginning at page 13 line 11 with the following:

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AK Processor based system 400 may further comprise various input/output (I/O) devices to communicate with a user. For example, processor based system 400 may communicate I/O information to a SAN administrator to facilitate management of SAN 105. Processor based system 400 includes I/O controller card [[404]] 405, communications adapter card 411, user interface card 408, and display card 409. I/O controller card 405 connects to storage devices 406, such as one or more of hard drive, CD drive, floppy disk drive, tape drive, to processor based system 400. Communications card 411 is adapted to couple processor based system 400 to network 412 which may be part of or coupled to LAN 104, for example. User interface card 408 couples user input devices, such as keyboard 413 and pointing device 407, to processor based system 400. Display card 409 is driven by CPU 401 to control the display on display device 410.

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